

Taiwan Semiconductor

# 2A, 200V - 1000V Fast Recovery Surface Mount Rectifier

# **FEATURES**

- AEC-Q101 qualified
- Glass passivated chip junction
- Ideal for automated placement
- Low power loss, high efficiency
- · Fast switching for high efficiency
- Low profile package
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free

| ΔDDI | ICATIONS | S |
|------|----------|---|

- Freewheeling
- Snubber
- DC/DC converters
- Automotive application

#### **MECHANICAL DATA**

• Case: SOD-128

• Molding compound meets UL 94V-0 flammability rating

• Terminal: Matte tin plated leads, solderable per J-STD-002

Meet JESD 201 class 2 whisker test

Polarity: Indicated by cathode band
Note 10.007

• Weight: 0.027g (approximately)

| KEY PARAMETERS     |            |      |  |  |
|--------------------|------------|------|--|--|
| PARAMETER          | VALUE      | UNIT |  |  |
| I <sub>F</sub>     | 2          | Α    |  |  |
| V <sub>RRM</sub>   | 200 - 1000 | V    |  |  |
| I <sub>FSM</sub>   | 50         | Α    |  |  |
| T <sub>J</sub> MAX | 175        | °C   |  |  |
| Package            | SOD-128    |      |  |  |
| Configuration      | Single die |      |  |  |





**SOD-128** 



| ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)      |           |                     |             |        |        |        |        |      |
|--|-----------|---------------------|-------------|--------|--------|--------|--------|------|
| PARAMETER  |           | SYMBOL              | RS2D        | RS2G   | RS2J   | RS2K   | RS2M   | UNIT |
|  |           |                     | FSH         | FSH    | FSH    | FSH    | FSH    |      |
| Marking code on the device   |           |                     | RS2DFH      | RS2GFH | RS2JFH | RS2KFH | RS2MFH |      |
| Repetitive peak reverse voltage  |           | $V_{RRM}$           | 200         | 400    | 600    | 800    | 1000   | V    |
| Reverse voltage, total rms value   |           | V <sub>R(RMS)</sub> | 140         | 280    | 420    | 560    | 700    | V    |
| Forward current  |           | l <sub>F</sub>      | 2           |        |        |        | Α      |      |
| Surge peak forward current, single half sine-wave superimposed on rated load | t = 8.3ms | l=                  |             |        | 50     |        |        | Α    |
|  | t = 1.0ms | IFSM                |             |        | 140    |        |        | Α    |
| Junction temperature   |           | TJ                  | -55 to +175 |        |        |        | °C     |      |
| Storage temperature  |           | T <sub>STG</sub>    | -55 to +175 |        |        |        | °C     |      |



Taiwan Semiconductor

| THERMAL PERFORMANCE                    |                  |     |      |  |  |
|--|------------------|-----|------|--|--|
| PARAMETER                              | SYMBOL           | TYP | UNIT |  |  |
| Junction-to-lead thermal resistance    | R <sub>eJL</sub> | 16  | °C/W |  |  |
| Junction-to-ambient thermal resistance | Reja             | 73  | °C/W |  |  |
| Junction-to-case thermal resistance    | Rejc             | 14  | °C/W |  |  |

**Thermal Performance Note:** Units mounted on PCB (5mm x 5mm Cu pad test board)

| PARAMETER   |                               | CONDITIONS  | SYMBOL           | TYP  | MAX  | UNIT |
|---|-------------------------------|---|------------------|------|------|------|
| Forward voltage <sup>(1)</sup>                        |                               | I <sub>F</sub> = 1A, T <sub>J</sub> = 25°C        |                  | 0.93 | -    | V    |
|   | RS2DFSH                       | I <sub>F</sub> = 2A, T <sub>J</sub> = 25°C        |                  | 1.01 | 1.30 | V    |
|   | RS2GFSH<br>RS2JFSH            | I <sub>F</sub> = 1A, T <sub>J</sub> = 125°C       |                  | 0.78 | -    | V    |
|   |                               | I <sub>F</sub> = 2A, T <sub>J</sub> = 125°C       |                  | 0.88 | 1.02 | V    |
|   |                               | I <sub>F</sub> = 1A, T <sub>J</sub> = 25°C        | V <sub>F</sub>   | 0.98 | -    | V    |
|   | RS2KFSH                       | I <sub>F</sub> = 2A, T <sub>J</sub> = 25°C        |                  | 1.06 | 1.30 | V    |
|   | RS2MFSH                       | I <sub>F</sub> = 1A, T <sub>J</sub> = 125°C       |                  | 0.83 | -    | V    |
|   |                               | I <sub>F</sub> = 2A, T <sub>J</sub> = 125°C       |                  | 0.93 | 1.05 | V    |
| Reverse current @ rated V <sub>R</sub> <sup>(2)</sup> |                               | T <sub>J</sub> = 25°C                             |                  | -    | 1    | μΑ   |
|   |                               | T <sub>J</sub> = 125°C                            | - I <sub>R</sub> | -    | 40   | μA   |
|   | RS2DFSH<br>RS2GFSH            |   | t <sub>rr</sub>  | -    | 150  | ns   |
| Reverse recovery time                                 | RS2JFSH                       | $I_{F} = 0.5A, I_{R} = 1.0A,$<br>$I_{rr} = 0.25A$ |                  | -    | 250  | ns   |
|   | RS2KFSH<br>RS2MFSH            | 111 - 0.207                                       |                  | -    | 500  | ns   |
| Junction capacitance                                  | RS2DFSH<br>RS2GFSH<br>RS2JFSH | 1MHz, V <sub>R</sub> = 4.0V                       | CJ               | 11   | -    | pF   |
|   | RS2KFSH<br>RS2MFSH            |   |                  | 10   | -    | pF   |

# Notes:

- (1) Pulse test with PW = 0.3ms
- (2) Pulse test with PW = 30ms

| ORDERING INFORMATION         |         |                      |  |  |  |
|------------------------------|---------|----------------------|--|--|--|
| ORDERING CODE <sup>(1)</sup> | PACKAGE | PACKING              |  |  |  |
| RS2xFSH                      | SOD-128 | 14,000 / Tape & Reel |  |  |  |

# Notes:

(1) "x" defines voltage from 200V(RS2DFSH) to 1000V(RS2MFSH)



#### **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

Fig.1 Forward Current Derating Curve

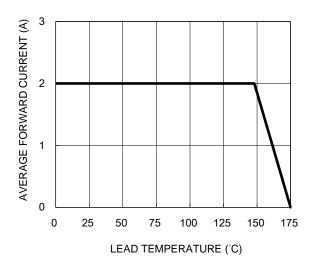


Fig.3 Typical Reverse Characteristics

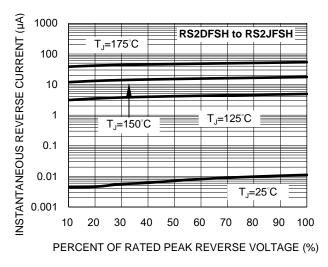


Fig.5 Typical Reverse Characteristics

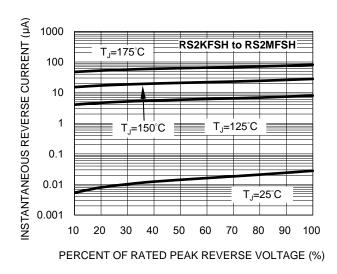


Fig.2 Typical Junction Capacitance

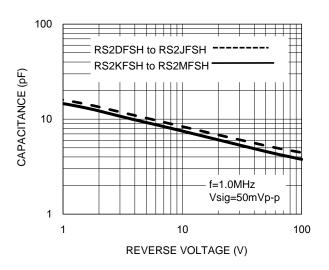


Fig.4 Typical Forward Characteristics

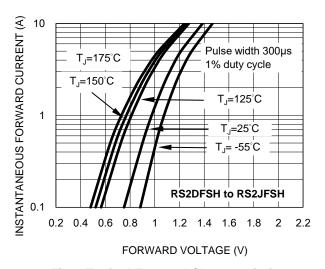
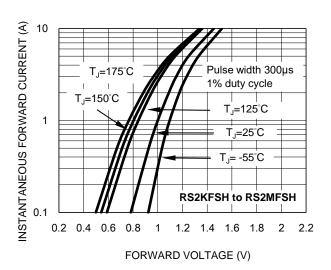


Fig.6 Typical Forward Characteristics

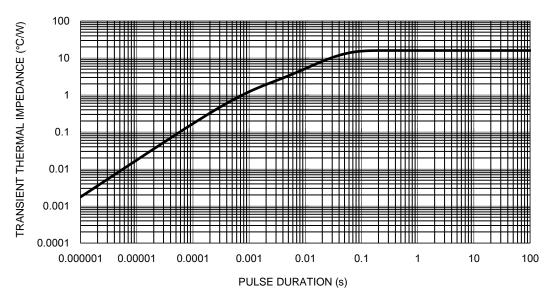




# **CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25°C unless otherwise noted)

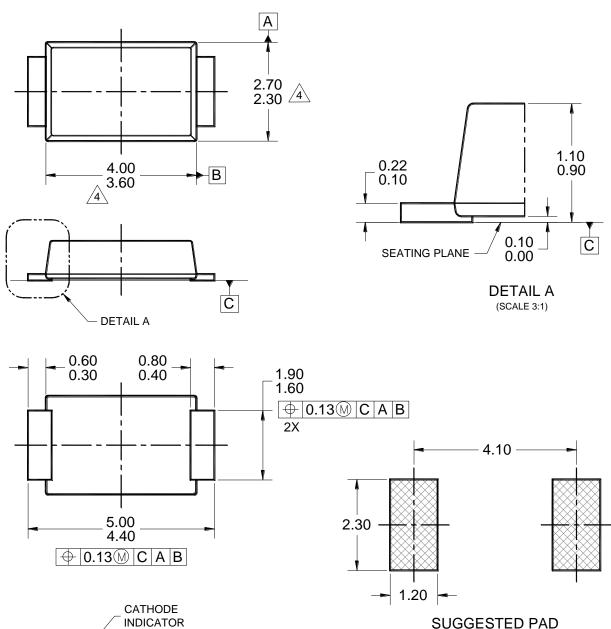
Fig.7 Typical Transient Thermal Impedance

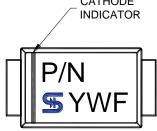




# **PACKAGE OUTLINE DIMENSIONS**

# **SOD-128**





# MARKING DIAGRAM

P/N = MARKING CODE YW = DATE CODE

F = FACTORY CODE

# NOTES: UNLESS OTHERWISE SPECIFIED

- 1. ALL DIMENSIONS ARE IN MILLIMETERS.
- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-2009.

**LAYOUT** 

- 3. PACKAGE OUTLINE REFERENCE: JEDEC DO-221, VARIATION AD, ISSUE B.
- MODED PLASTIC BODY DIMENSIONS DO NOT INCLUDE MOLD FLASH.
  - 5. DWG NO. REF: HQ2SD07-SOD128-039 REV A.



Taiwan Semiconductor

# **Notice**

Specifications of the products displayed herein are subject to change without notice. TSC or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies.

Purchasers are solely responsible for the choice, selection, and use of TSC products and TSC assumes no liability for application assistance or the design of Purchasers' products.

Information contained herein is intended to provide a product description only. No license, express or implied, to any intellectual property rights is granted by this document. Except as provided in TSC's terms and conditions of sale for such products, TSC assumes no liability whatsoever, and disclaims any express or implied warranty, relating to sale and/or use of TSC products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify TSC for any damages resulting from such improper use or sale.